

CEN WORKS ON REAL TIME SIMULATION

SIMÓN VELOSO COORDINADOR ELÉCTRICO NACIONAL (CEN, CHILEAN'S ISO)



CONTENTS

- Background
- Infrastructure
- Projects
 - Testing of System Integrity Protection Scheme (SIPS)
 - Chilean HVDC Model
 - Parameter Identification (standard models)
 - Hybrid Simulations



BACKGROUND

• According to a law modification in 2018, "the Chilean ISO will have permanent resources to develop and to coordinate research in energy matters aiming to enhance the real time operation of the power system".

Specific goals to fulfil what was mandated by law:

- To contribute with new technologies
- To study the behaviour of equipment and to de-risk its operation before putting it in service
- To train employees in the theory and use of new technologies
- To promote research through collaboration with universities, study centers and consulting groups.

INFRASTRUCTURE



- NovaCor with 4 licensed cores
- GTNETx2 card
 - Port 1: 104, GOOSE, PLAYBACK, PMU, Socket
 - Port 2: DNP3, MODBUS, Socket
- Synchronizer
- Scope
- 2 amplifiers
- SEL-3555 (RTAC)
- SEL-2240 Axion



INFRASTRUCTURE



• PSAT (Static)

• TSAT (RMS)

• TRI (TSAT-RTDS Interface)



System Integrity Protection Scheme

Also known as Remedial Action Scheme (RAS) by NERC



USER SPOTLIGHT SERIES BY

Equipment RTAC SEL-3555

Programming software SEL AcSELerator RTAC

GTNETx2 DNP3 port

IIIRT

Technologies





• Renewable power plant simplified model









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5	La Cebada - Pan de Azúcar	62.3	448	385.6	0	2	120	241	3	160	0	0	530.1	0	0		0	0	0	0	0	<u> </u>	0	0	0	0	0	49.9	0	0	191.3	3 0		0
6	La Cebada - Punta Sierra	61.8	448	386.1	0	4	241	241	7	160	0	0	144.8	0	0	0	0	0	0	•	0	•	0	0	0	0	0	49.9	0	0	191.3	3 0	<u> </u>	0
7	Punta Sierra - Las Palmas	61.8	448	386.1	0	4	241	241	7	160	0	0	144.9	0	0	0	0	0	0	0	0	0	0	0	0	0	0	49.9	0	0	191.3	3 0		0
8	Las Palmas - Los Vilos	57.6	448	390.3	0	4	241	241	7	160	0	0	149	0	0	0	0	0	0	0	0	0	0	0	0	0	0	49.9	0	0	191.3	3 0		0
9	Los Vilos - Nogales	57	448	390.9	0	4	241	241	7	160	0	0	149.6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	49.9	0	0	191.3	3 0	0	0
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Remarks

- Hardware in the loop testing.
- Assessment of the dynamic and steady state performance of a high level algorithm (wide area).
- Future algorithms will be tested in the RTDS platform to increase its reliability.
- Upcoming algorithms will include simultaneously optimization of transmission lines ampacity as well as wide area voltage or reactive power control.
- The flexibility of the RTDS will allow testing of different communication protocols or equipment.



- Bipole HVDC \pm 600 kV
- ~ 1500 km
- Two terminals
- Converter stations of 2000 MW
- Scheduled for 2027



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• Distributed VAR Compensation (SVC and STATCOM)









3ph short circuit at Lo Aguirre 500 kV HVDC out of service



3ph short circuit at Lo Aguirre 500 kV HVDC in service



HVDC link in service SCR = 4 (at rectifier station)



HVDC link in service SCR = 12.5 (at inverter station)







About 300 models in the Chilean official RMS database

- Synchronous machines (govs, avr, pss, uel and oel)
 - Hydraulic
 - Steam turbines
 - Combined cycle
- Based in power electronics:
 - Wind generators
 - Photovoltaic
 - Solar thermal

USER SPOTLIGHT SERIES BY

• STATCOMs and SVCs

pcu_RALCO: Regulador de Velocidad



- Modeled in 2005
- PowerFactory (DIgSILENT)



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- Modeled in 2005
- PowerFactory (DIgSILENT)
- Out of TSAT standard models
- Due to valve position modeling



pcu_RALCO1: Regulador de Velocidad



- Modeled in 2005
- PowerFactory (DIgSILENT)
- Out of TSAT standard models
- Due to valve position modeling
- Search for the equivalent model (parameter identification)





- Particle Swarm Optimization (PSO)
- Equivalent dynamic models could be obtained massively
- It will allow consultants to access the power system database in a standard form













2 deg difference before connection







Event: power plant outage (180 MW)







EMT and RMS models interconnected

Transferred power ~ 0 MW

RMS integration step = 0.25 cycles



RMS integration step = 1 cycle



EMT and RMS models interconnected

Transferred power ~ 100 MW (towards RMS model)

RMS integration step = 0.25 cycles



RMS integration step = 1 cycle





CEN WORKS ON REAL TIME SIMULATION

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