

# Hitachi Energy HVDC C & P Factory System Test

Saudia Arabia – Egypt Project and RTDS Wind Turbine model Validation Adnan Azmat – Senior Test Engineer – Tender and Conceptual Design – Control and Protection



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# Agenda

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- 1. FST (Factory System Test)
- 2. Saudi-Egypt Multi-terminal HVDC
- 3. Generic Wind Farm model Validation



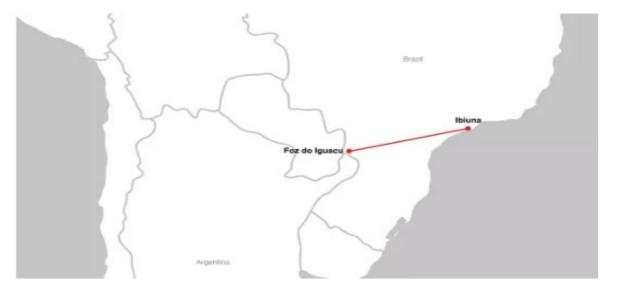
## FST – Factory System Test



#### History

- Itaipu 6300 MW
- Analogue Simulator
- Regular use of Real-Time Digital simulation for C & P testing since 2004

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#### Main data

Commissioning year:	1984 -1990
Configuration:	Bipole
Power rating:	6,300 MW
Direct voltage:	±600 kV
Application:	Connecting remote generation

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## Some of our projects in FST

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#### Main Purpose

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- Verify that the C & P equipment meets design specifications
- Verify the function and coordination of control and protection actions
- Performance analysis of C & P system with Real-time HIL system

#### **Consequential Benefits**

- Shorten time for commissioning and plant "burn-in"
- Identify and solve problems as early as possible in the process
- Minimize disturbances to the AC-system during commissioning





MACH control and protection system is assembled and connected to a real-time simulator

The set-up includes:

- **Control and Protection systems** ٠
- Operator interface and interface to the customer SCADA. ٠



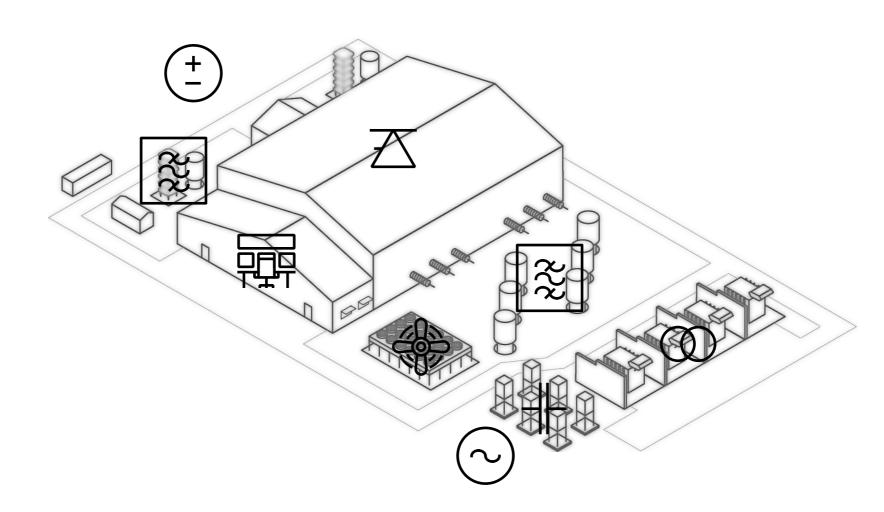


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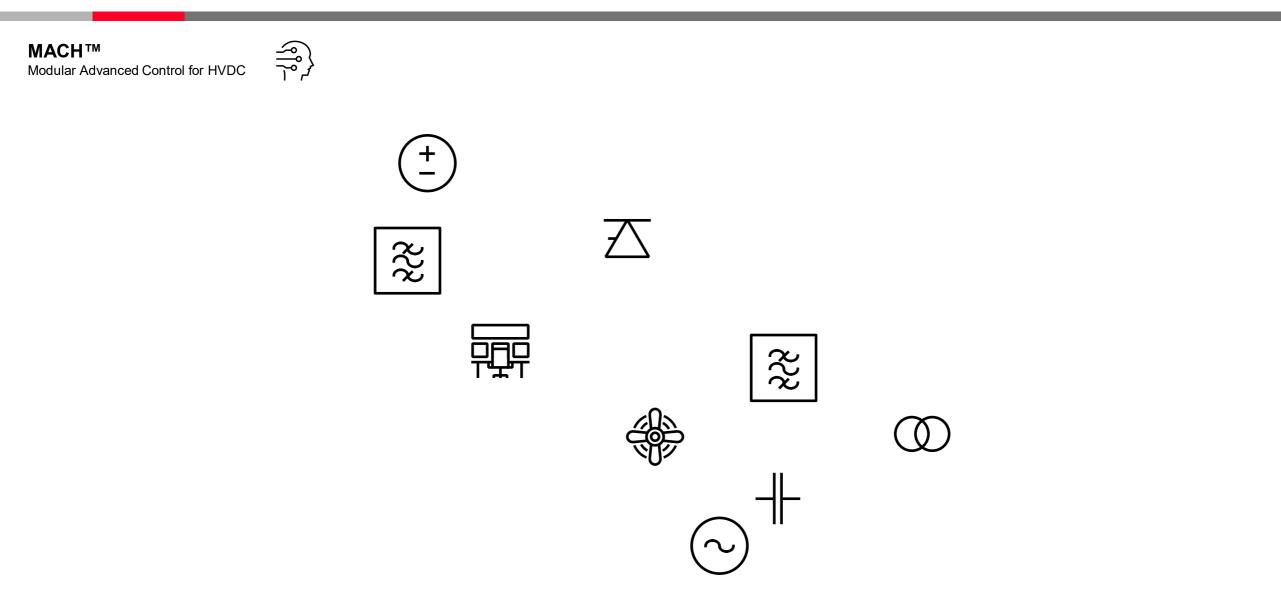
MACH<sup>™</sup> Modular Advanced Control for HVDC

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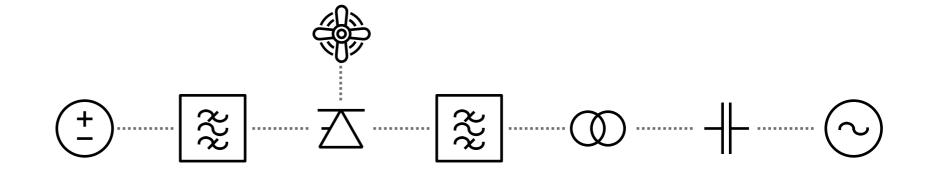
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MACH<sup>™</sup> Modular Advanced Control for HVDC

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MACH<sup>™</sup> Modular Advanced Control for HVDC

Valve point

Control point

Measurement point

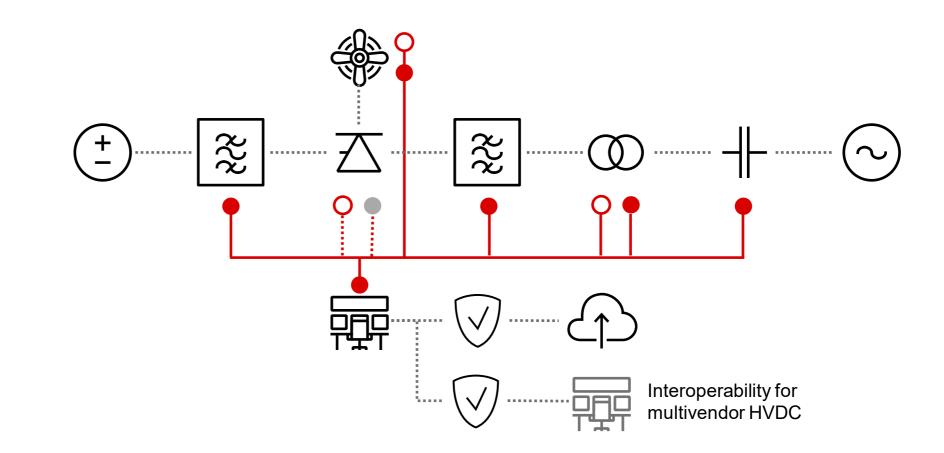
I/O communication

Valve communication

С

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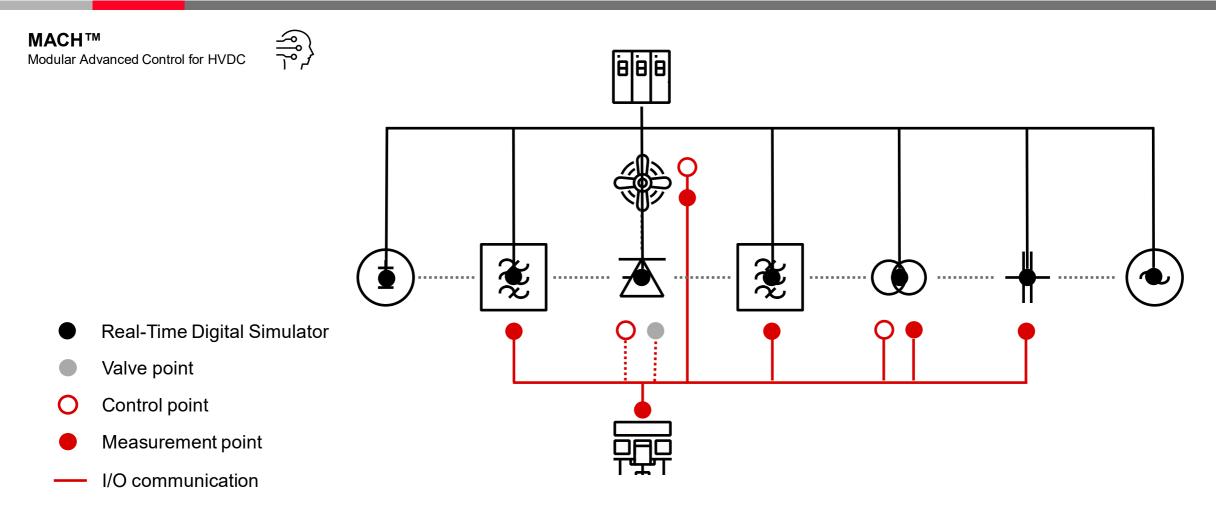
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## **HVDC Control & Protection application testing in FST**





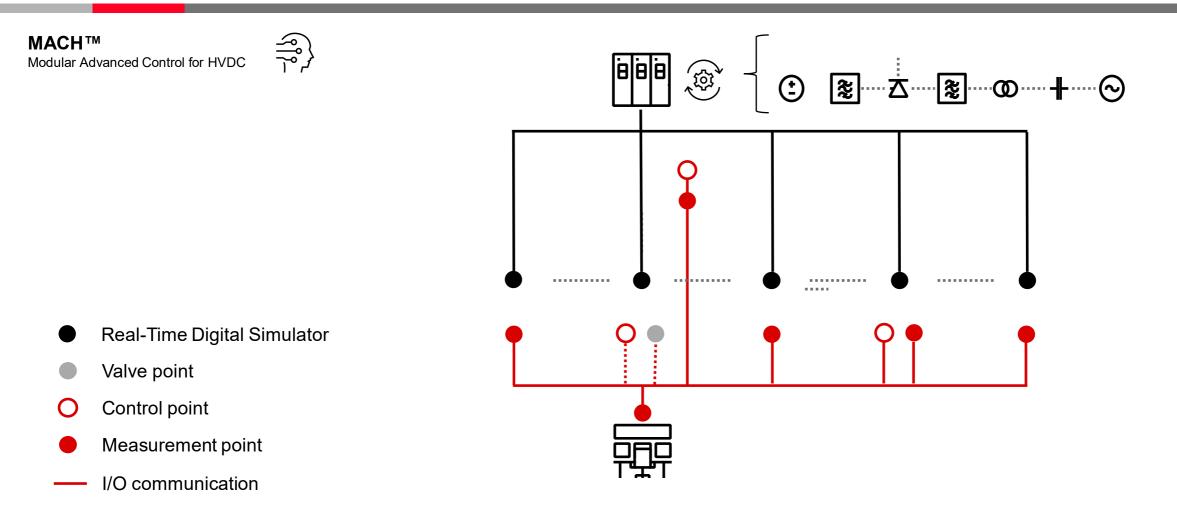
#### ······ Valve communication

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## **HVDC Control & Protection application testing in FST**

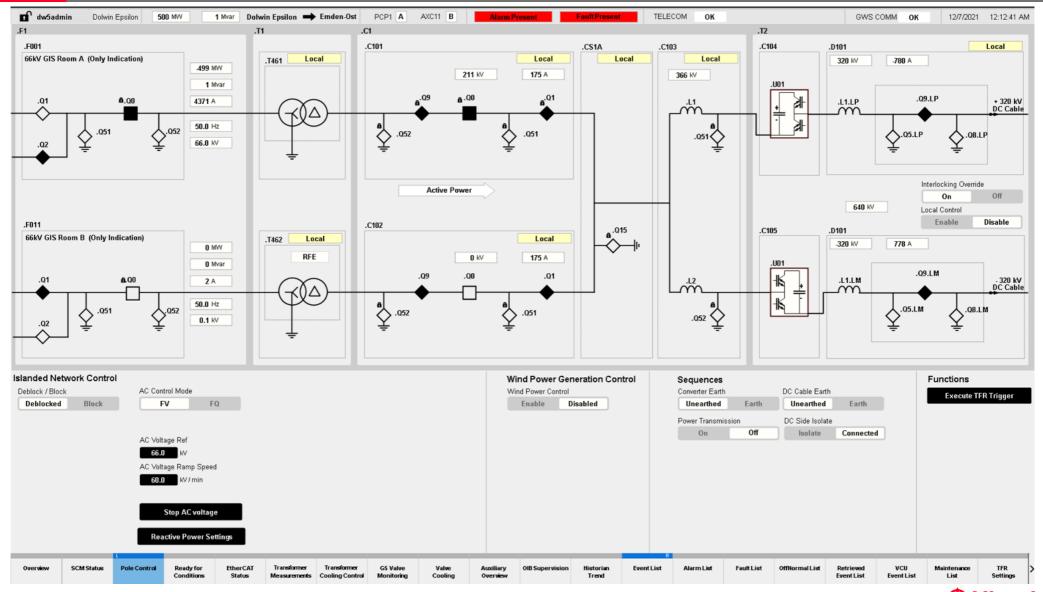




#### Valve communication

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## HMI (Human Machine Interface)



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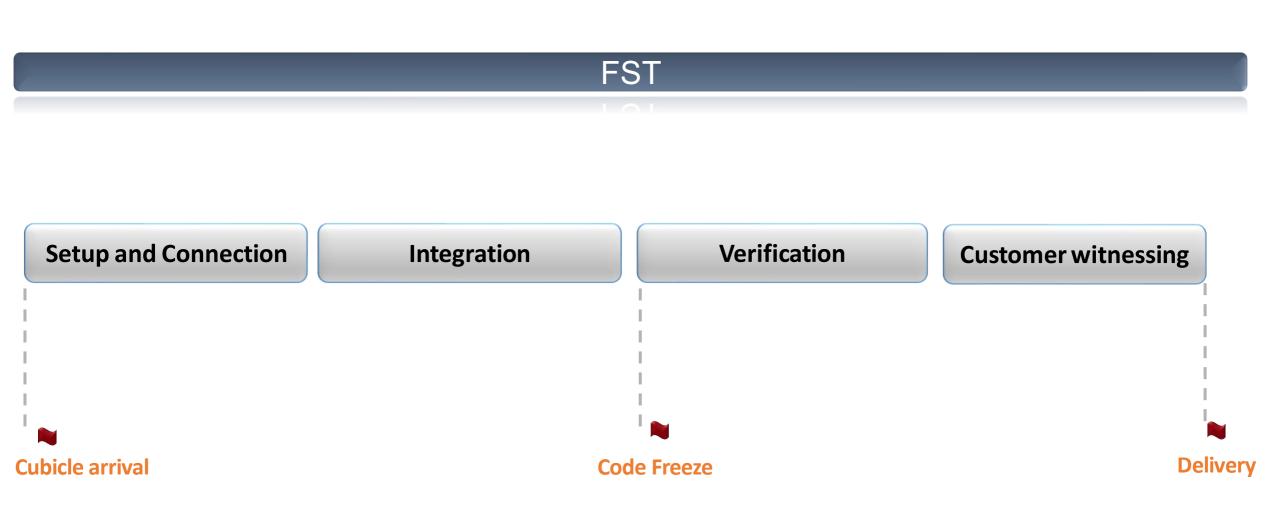
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## **FST** Testing



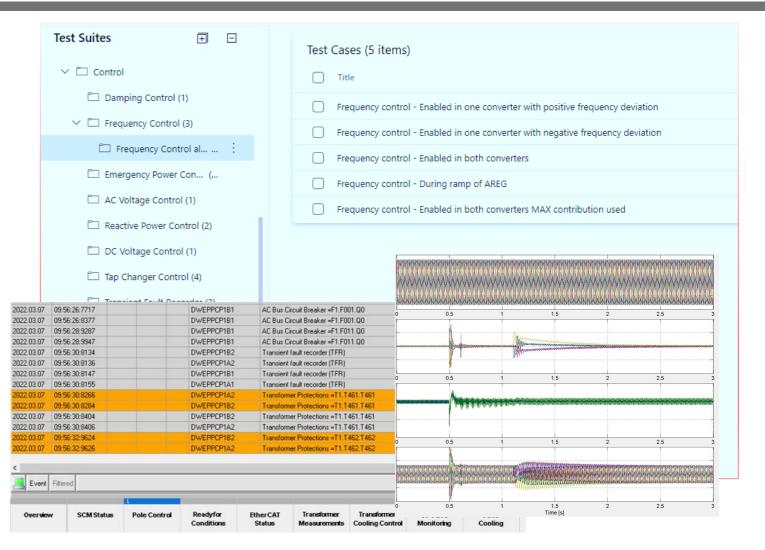
Integration and verification testing:

- Test instructions with defined steps and expected result
  - TFRs
  - Event lists

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HMI screenshots for pre- and post conditions





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Optimized





Upgradable

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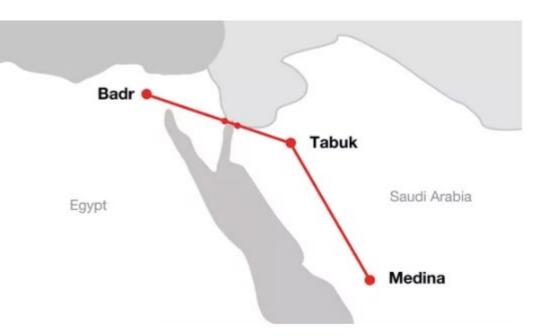


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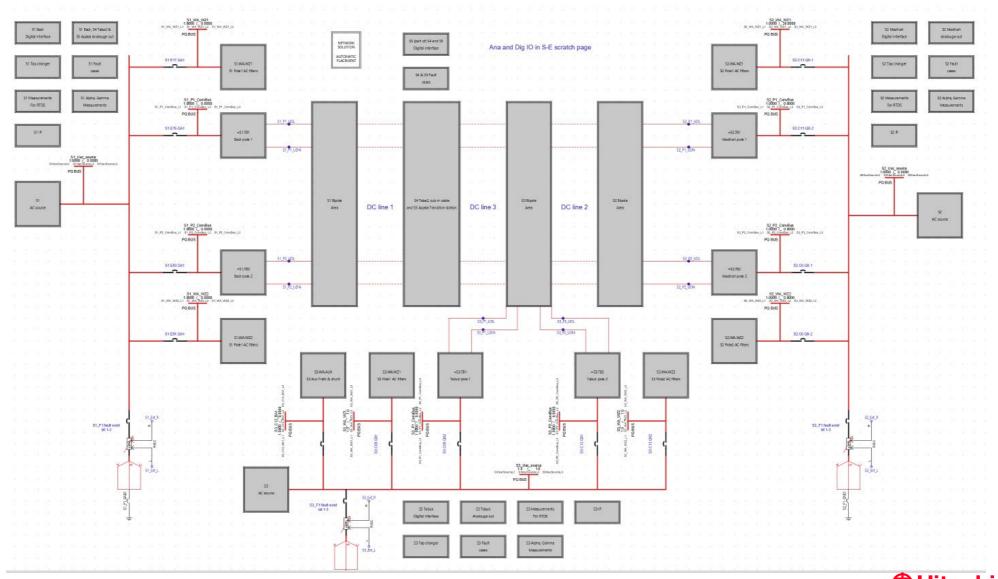
Main data	
Commissioning year:	2025
Configuration	Multi-terminal link
Power rating:	3,000 MW (multi-terminal) Madinah 2 *1500 MW (Saudi Arabia) Tabuk 2 *750 MW (Saudi Arabia) Badr 2 *1500 MW (Egypt)
AC voltage:	Madinah 380 kV (Saudi Arabia) Tabuk 380 kV (Saudi Arabia) Badr 500 kV (Egypt)
DC voltage:	±500 kV
Application:	Interconnecting grids



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## **RSCADFX** draft case

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## C & P IO Interface



#### Analogue Interface

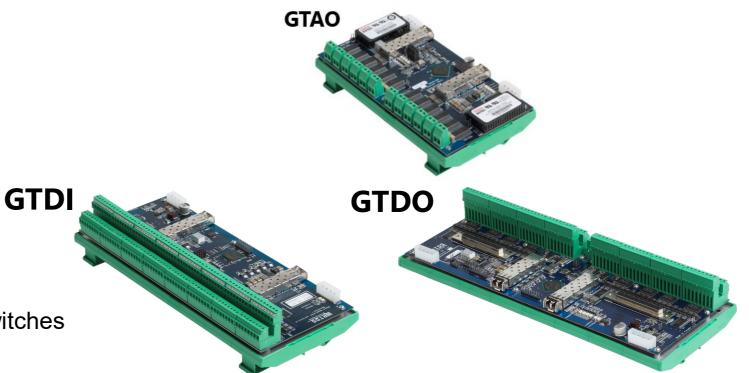
- 19 GTAO V2 •
- 18 Current amplifiers •

#### **Digital Interface**

- 7 GTDI V2 ٠
- 7 GTDO V2 ٠
- 145 Circuit Breakers •
- 89 Relays representing ground switches ٠

#### **Real-time processing power**

- 10 Cores (NOVACor) ٠
- ~300 Power system nodes in Simulation •





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#### Case

- Offshore wind power integration to the network
- No available specific wind farm model for PSCAD/RSCAD from supplier
- Effect of connected wind farm on Control and Protection system for HVDC
- Fulfillment of the grid code by performing dynamic disturbances

#### Simulation model

- Wind farm model connected to network equivalent represented by an infinite source
- Aim is to provide reasonable active and reactive power to point of common coupling

#### Wind farm model (Electrical main circuit)

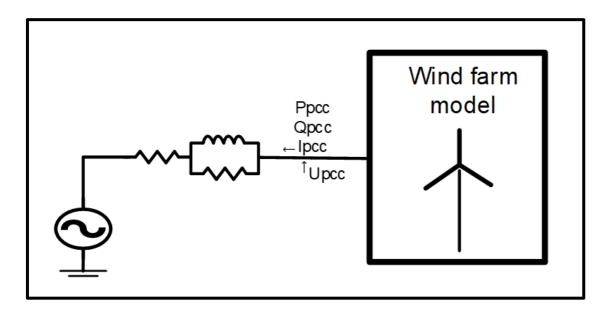
- Ideal voltage source controlled by phase, reactors and resistors
- Scalable components to adapt the test cases

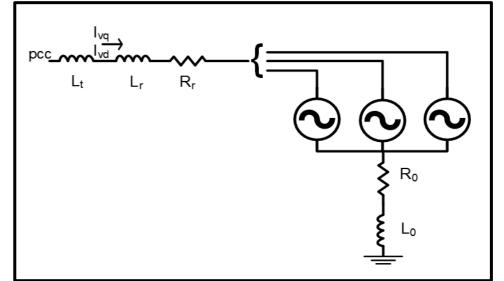
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Simulation model setup

Wind farm model





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### Test cases

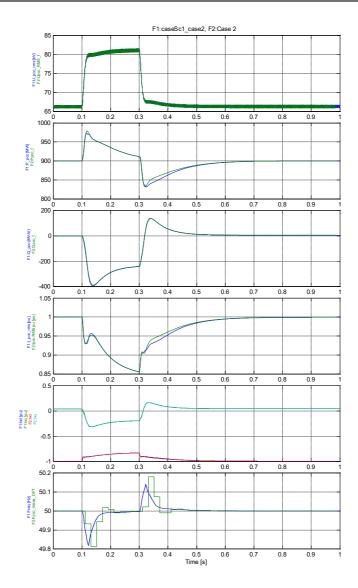


- AC Voltage step 1.25 p.u. for 200ms
- Frequency step 0.5 Hz
- Ramp frequency at rate 2 Hz/s
- Active power ramp down from nominal to 50% of nominal (EPC)
- 3PhG, 100 ms, 10% remaining voltage
- 1PhG, 100 ms, 50% remaining voltage



## AC Voltage step 1.25 p.u. for 200ms



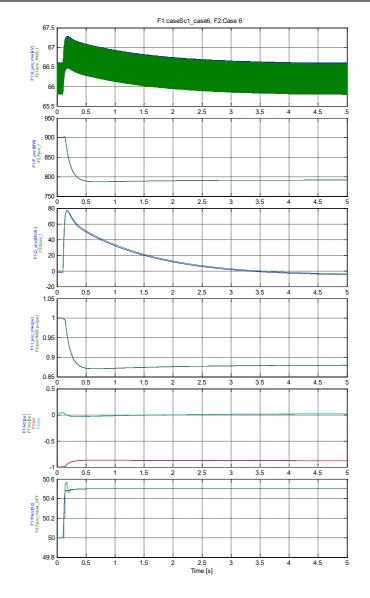


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## Frequency step 0.5 Hz



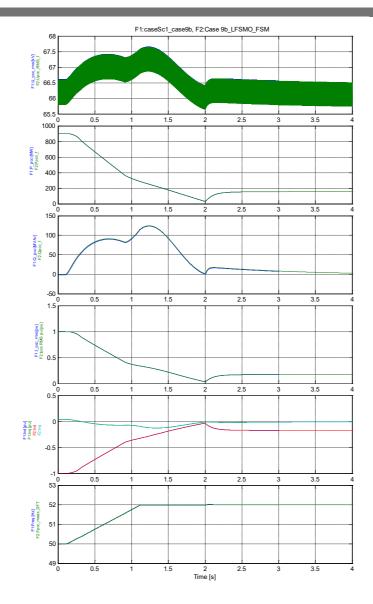


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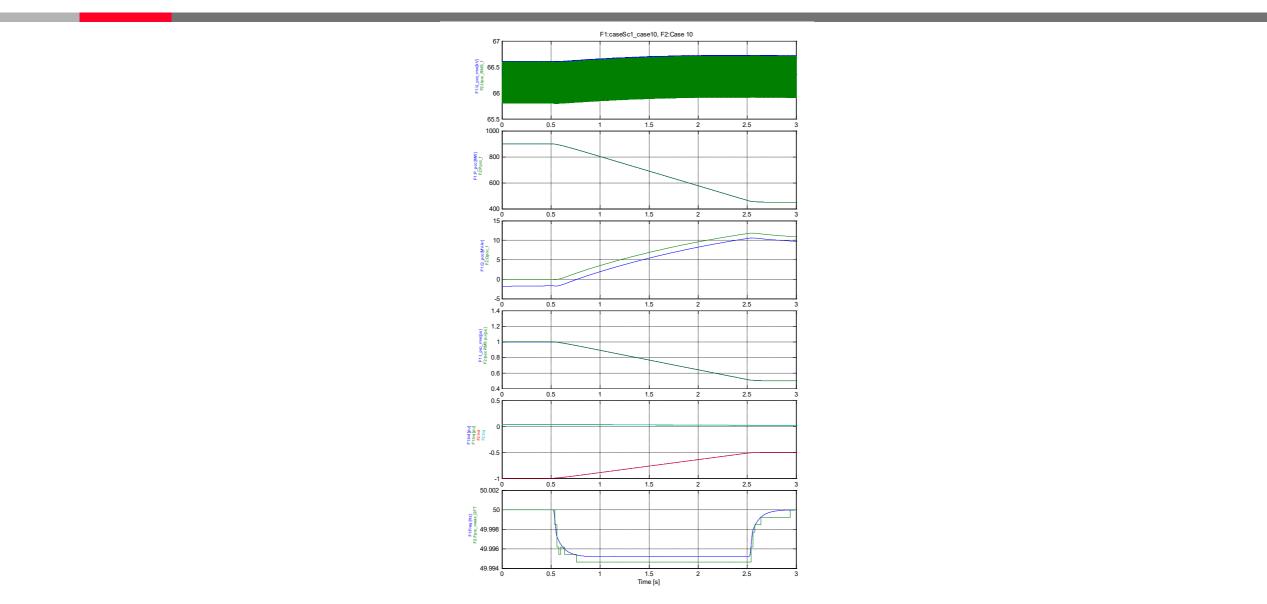
## Ramp frequency at rate 2 Hz/s





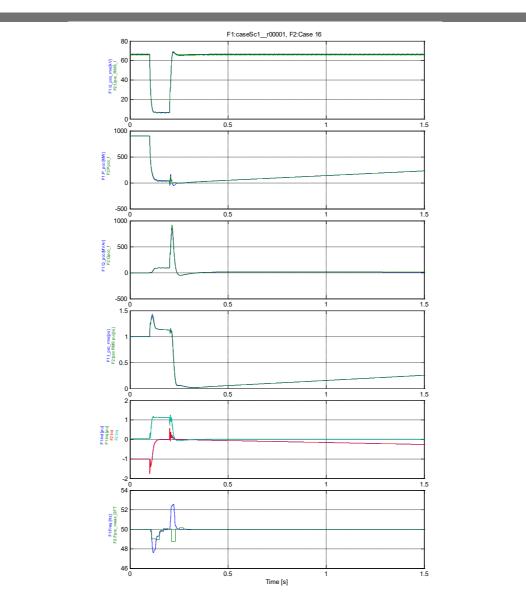
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## Active power ramp down from nominal to 50% of nominal (EPC)



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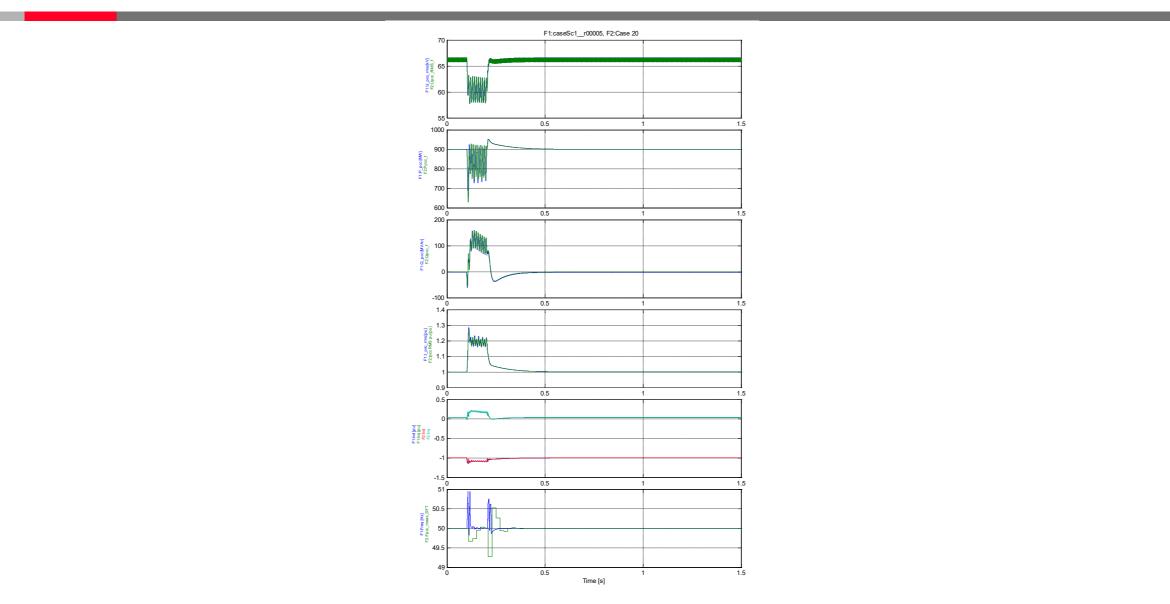
## 3 Phase-Ground, 100 ms, 10% remaining voltage



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## 1 Phase-Ground, 100 ms, 50% remaining voltage



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# **Discussion!**







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