REAL-TIME SIMULATION HARDWARE
FOR THE RTDS® SIMULATOR

Maintaining a real-time electromagnetic transient simulation with a timestep in the 50-microsecond range is much too demanding for a regular PC. A dedicated parallel processing hardware platform, called NOVACOR™, carries out the real-time simulation, while INPUT AND OUTPUT CARDS are used to create a hardware-in-the-loop testbed with external equipment.

NOVACOR: CENTRAL MULTICORE PROCESSING HARDWARE

NovaCor, the RTDS Simulator’s current hardware generation, is the custom-integrated parallel processing hardware responsible for carrying out the real-time simulation. World-class engineers and developers have worked to ensure that simulations run directly on the processor (no OS) for unparalleled speed and efficiency. Full control of the hardware platform allows RTDS Technologies to provide customers with higher-fidelity simulation and the best support in the industry.

SCALABILITY THROUGH CORE LICENSING

Inside each NovaCor chassis is a custom-built board featuring IBM®’s POWER8™ multicore processor. The processor includes 10 cores, any number of which can be licensed depending on the user’s simulation requirements and budget. A higher number of licensed cores means that larger and more complex circuits can be represented. Core licenses are provided digitally, meaning expansion is quick and easy.

FURTHER EXPANSION FOR LARGE-SCALE SIMULATION

Multiple NovaCor units can be used in parallel or can be connected together to represent larger networks. UP TO 144 UNITS can be fully connected to represent massive networks.

INPUT AND OUTPUT CARDS (I/O): CREATING A CLOSED-LOOP INTERFACE

I/O cards receive signals from the simulation or an external device, respectively, and interface the device under test to the simulated network in a closed loop. The RTDS Simulator’s I/O is highly scalable and is connected to the main simulation hardware via fibre optic cable.

ANALOGUE I/O

Analogue I/O cards have 12 channels each (16-bit). The cards operate at +/- 10 V peak. In the case that external equipment requires a higher-level signal (for example, secondary-level relay inputs), power amplifiers can be added to the interface. Each NovaCor chassis also includes 12 built-in analogue output channels (12-bit).
GTFPGA UNIT: MULTI-FUNCTION AUXILIARY SIMULATION HARDWARE

The GTFPGA Unit is an optional auxiliary hardware component that enhances the capabilities of the RTDS Simulator for certain applications. The rack-mountable enclosure houses a powerful FPGA board capable of high-speed calculations in parallel with the power system simulation running on the central RTDS Simulator processing hardware. The GTFPGA Unit connects to the main simulation hardware via fibre optic cable.

- Modular Multilevel Converter (MMC) simulation for HVDC and FACTS
- IEC 61850-9-2/61869-9 Sampled Values (SV) streaming
- Generic Power Electronics Solver for custom-topology converters
- Travelling wave relay testing (TWRT)
- Frequency dependent transmission line and cable modelling

DIGITAL I/O

Digital I/O cards have 64 channels each. The cards are designed for interfacing with external equipment in the +5 to +30 V\text{dc} range. Dry-contact input and output channels are also provided as part of the RTDS Simulator cubicle's front panel. The RTDS Simulator can provide breaker status output up to 250 V\text{dc}.

COMMUNICATION-BASED I/O

The GTNETx2 card, the RTDS Simulator’s network interface card, provides an interface to external equipment via various Ethernet-based communication protocols, including MODBUS, TCP/UDP, IEC 61850 GOOSE and Sampled Values, DNP3, IEC 60870-5-104, and synchrophasor data. Capabilities are provided on a firmware licensing basis.

NOVACOR PROCESSING HARDWARE IS INTERFACED TO ETERNAL PROTECTION EQUIPMENT VIA THE GTNETX2 CARD