



HIL TESTING AN ADAPTIVE OUT-OF-STEP PROTECTION ALGORITHM BASED ON WIDE-AREA MEASUREMENTS

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USER SPOTLIGHT SERIES 2.0 BY  RTDS
Technologies

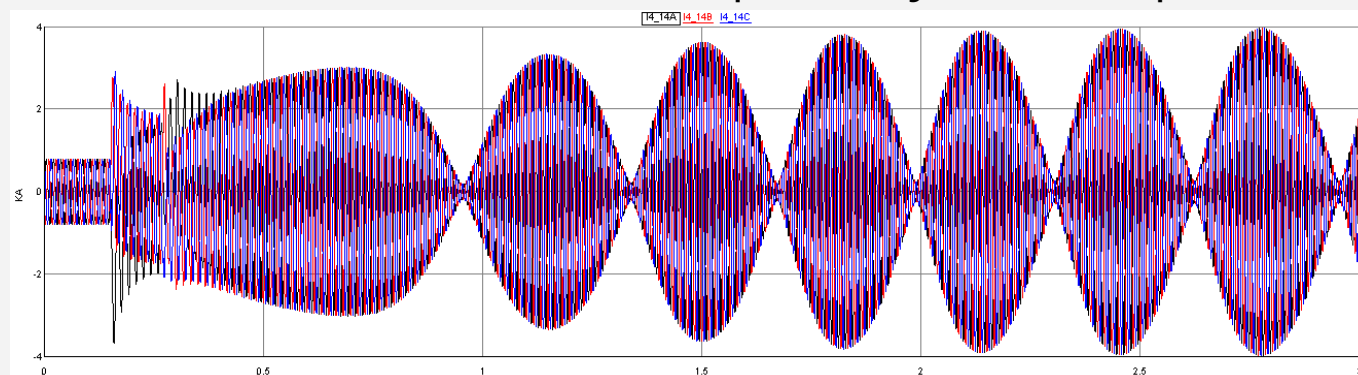
OUTLINE

- What is an out-of-step condition and conventionally used protection.
- The developed adaptive out-of-step protection concept.
- Hardware-in-the-Loop (HiL) test setup.
- Test results.
- Demonstration.

OUT-OF-STEP CONDITION

What is an out-of-step condition?

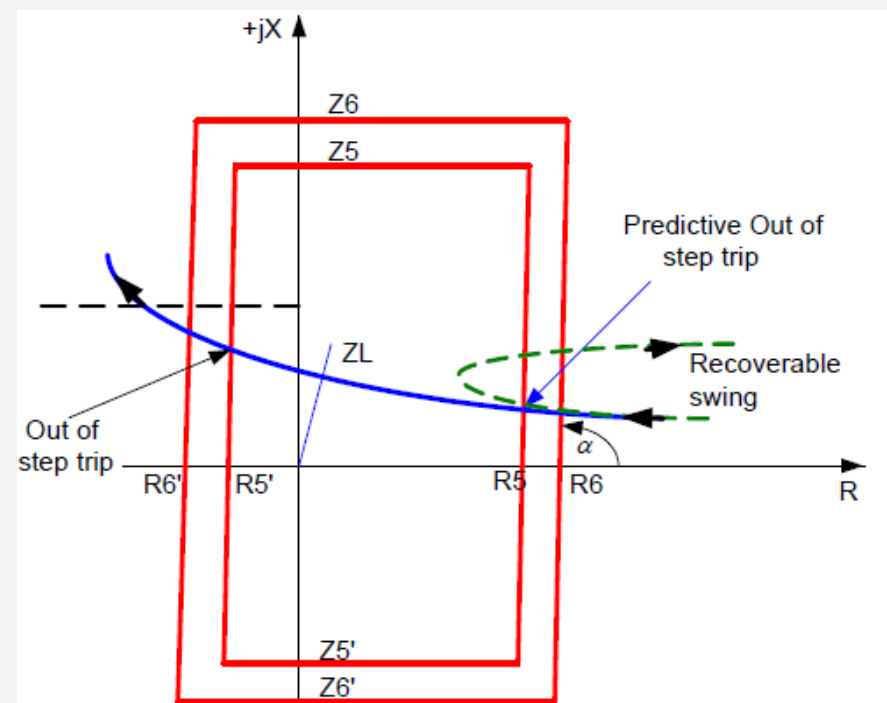
- Disturbances can propagate into a larger scale event, causing a major imbalance between the mechanical input and electrical output power of generators. This major imbalance can result in a loss of synchronism in the power system and is referred to as an out-of-step (OOS) condition.
- During the OOS condition large oscillations of currents and voltages occur, which cause additional mechanical and thermal stresses on power system components.



OUT-OF-STEP CONDITION

Protection?

- The conventional OOS protection is realized by impedance relays which use the apparent impedance seen by the relay to detect an OOS condition.
- These relays require functional settings to function properly.
- With more renewable energy sources being integrated into the power system, the generator composition becomes harder to predict. This complicates the calculation of settings further.
- A new OOS protection algorithm, using wide-area



THE DEVELOPED ADAPTIVE ALGORITHM

How it works?

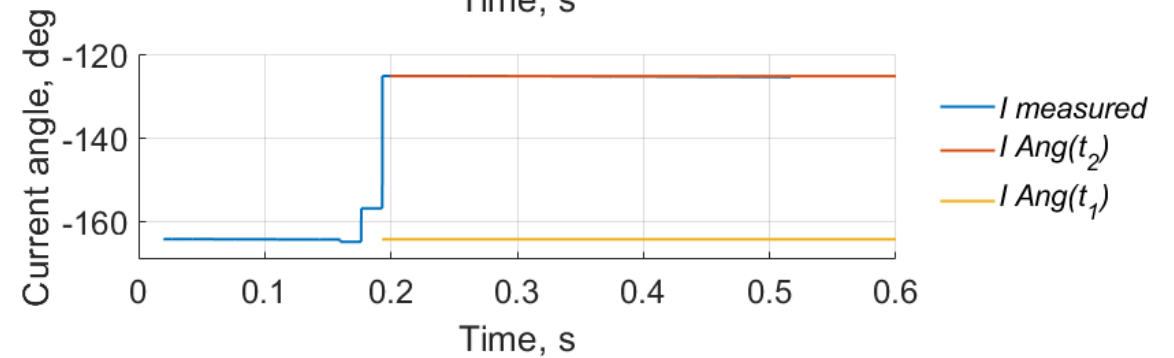
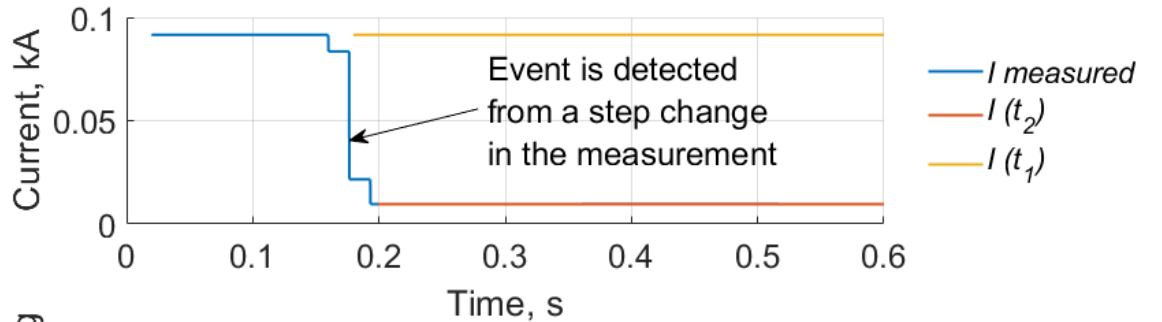
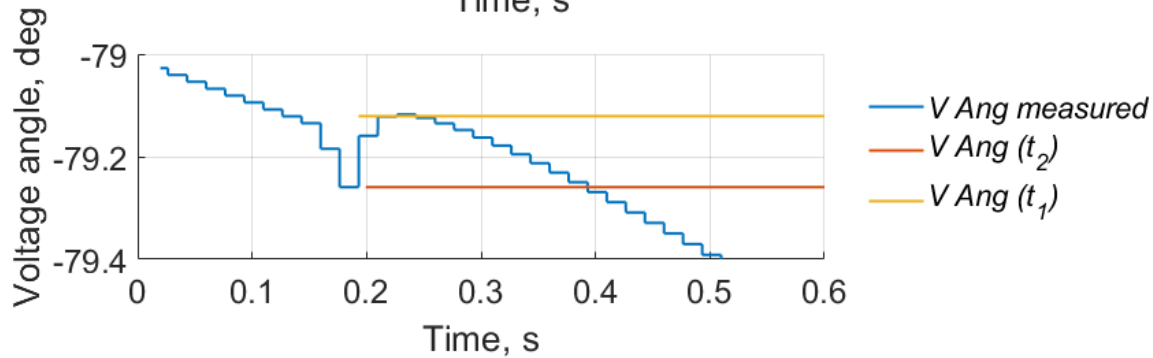
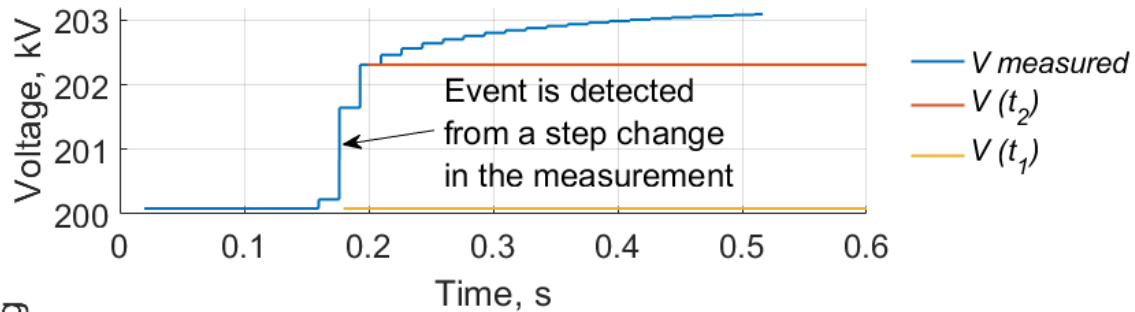
- The goal is to develop an easily implementable out-of-step tripping algorithm that is adaptive in real-time and requires minimal settings.
- The developed algorithm relies on the computation of system impedances seen from remote ends of a transmission line.
- To compute the system impedances on seen from the remote ends of transmission lines the following equation is used [1]:

$$\underline{Z}_{eq} = \frac{\underline{V}(t_2) - \underline{V}(t_1)}{\underline{I}(t_2) - \underline{I}(t_1)} = -\frac{\underline{\Delta V}}{\underline{\Delta I}}$$

[1] K. O. H. Pedersen, A. H. Nielsen, and N. K. Poulsen, "Short-circuit impedance measurement," IEE Proceedings - Generation, Transmission and Distribution, vol. 150, no. 2, pp. 169–174, 2003.

THE NEW ADAPTIVE ALGORITHM (1)

How it works?



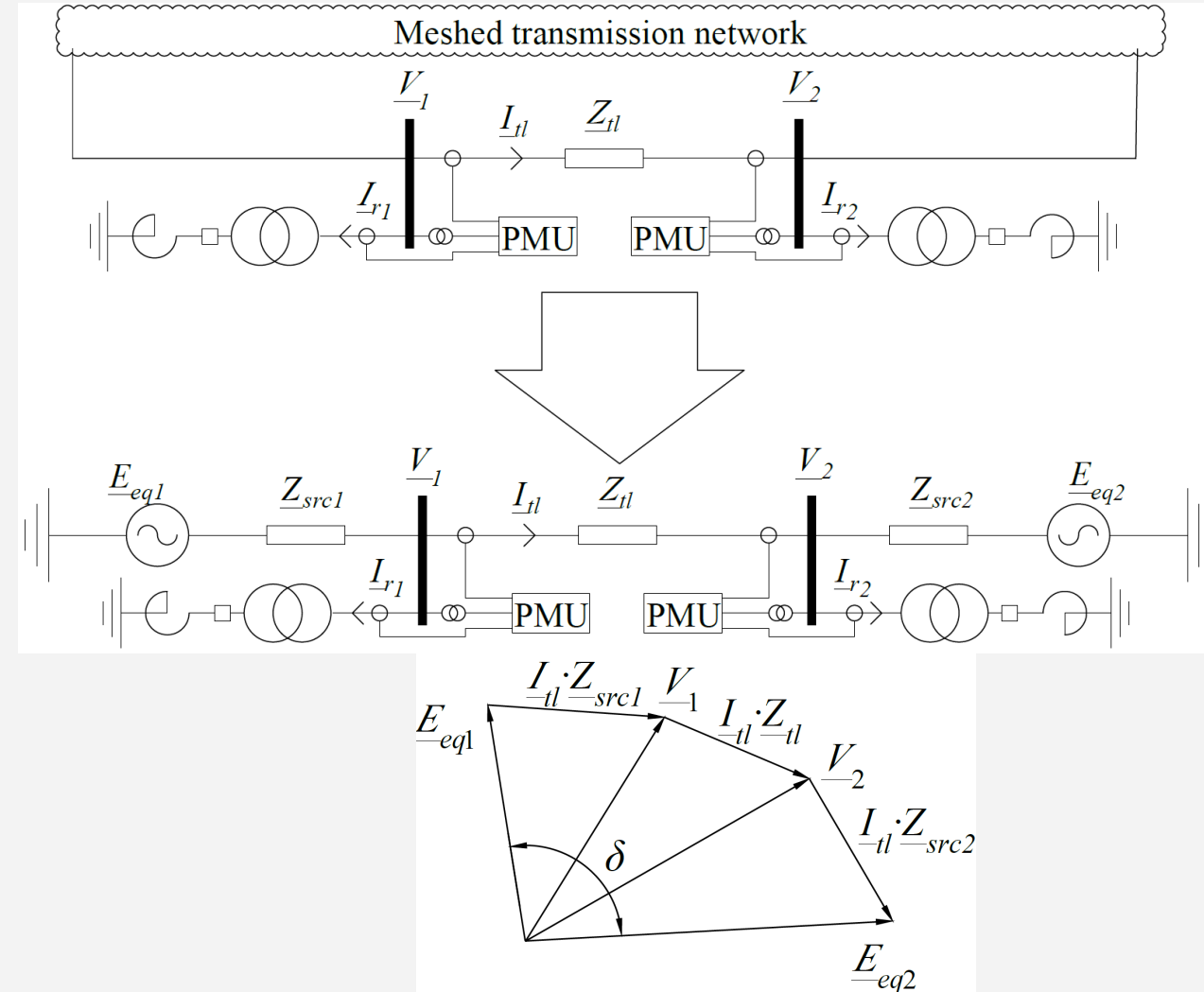
THE NEW ADAPTIVE ALGORITHM (2)

How it works?

- Full system impedance is computed on the bus and reduced to only \underline{Z}_{src} on both ends of the transmission lines by using the following formula:

$$\underline{Z}_{src1} = -\frac{V_1(t_2) - V_1(t_1)}{I_{r1}(t_2) - I_{r1}(t_1)} \cdot \frac{I_{r1}(t_1) - I_{r1}(t_2)}{(I_{r1}(t_1) - I_{r1}(t_2)) - (I_{tl}(t_1) - I_{tl}(t_2))} =$$

$$-\frac{\Delta V_1}{\Delta I_1} \frac{\Delta I_{r1}}{\Delta I_{r1} - \Delta I_{tl}} = \frac{\Delta V_1}{\Delta I_{r1} - \Delta I_{tl}}$$

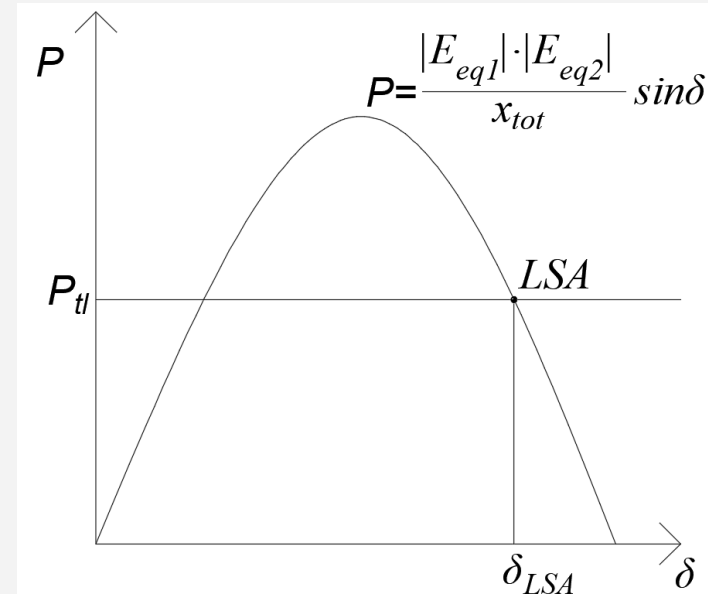


THE NEW ADAPTIVE ALGORITHM (3)

How it works?

- From computed system impedances a power-angle curve is built.
- On power-angle curve the last stable angle is fixed by using the computed angle difference between the sources ($90^\circ < LSA < 130^\circ$).
- The angle δ between two equivalent sources is computed using measured voltages and currents and computed impedances.
- Operation criteria:

$$\left\{ \begin{array}{l} \delta > \delta_{LSA} \text{ for two consecutive measurements} \\ \frac{d\delta}{dt} > 0 \text{ for two consecutive measurements} \\ V_1 > 0.5 \text{ pu} \\ V_2 > 0.5 \text{ pu} \\ \frac{d\delta}{dt} < 20\pi \text{ rad for two consecutive measurements} \end{array} \right.$$



HIL TEST SETUP

RTDS setup at TUDelft

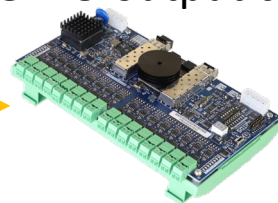


GTFPI interface



Trip signals to HV panel

GTAO output card



±10V
analogue
signal



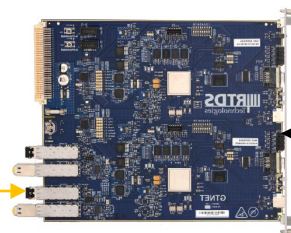
Power amplifier

±100V
analogue
signal



Impedance-based
protection relays

GTNETx2 card



C37.118 data

IEC 61850 data



C37.118 data

IEC 61850 data



External controller
with running the
developed algorithm

IRIG-B time signal



PTP time
signal



NTP time signal

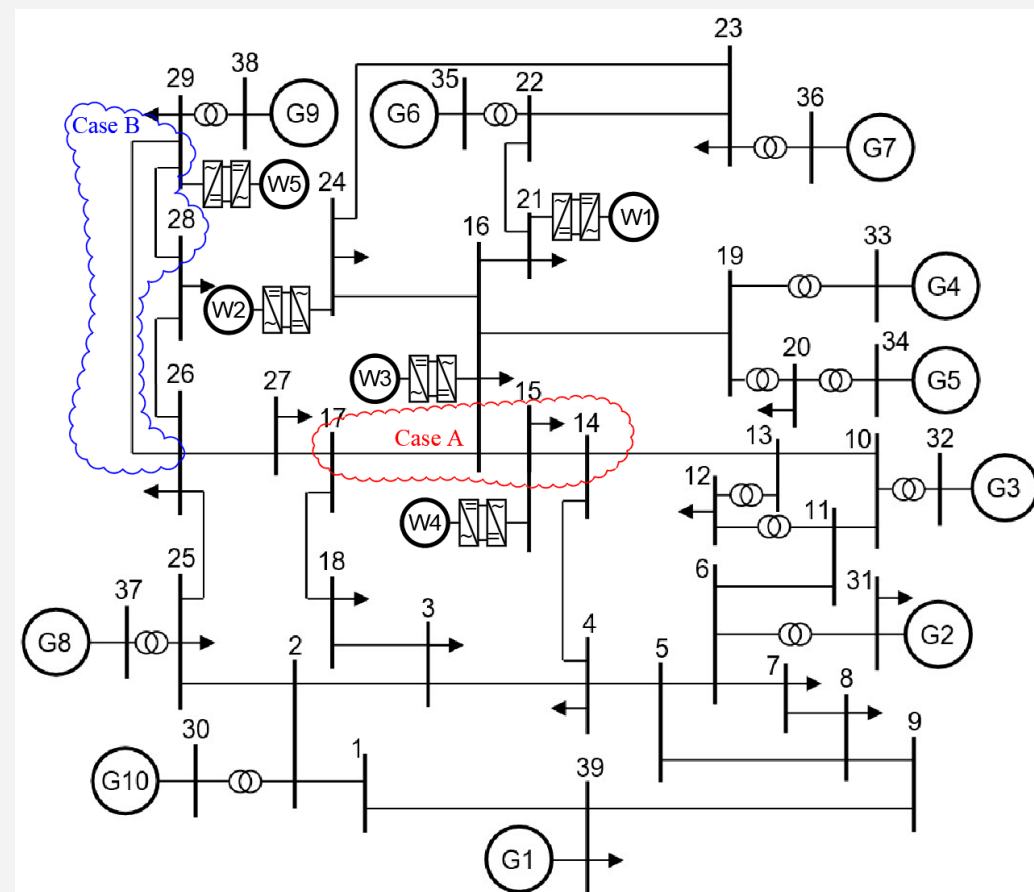
Meinberg Synbox

Seven Solutions atomic clock slave unit

HIL TEST SETUP

Test network

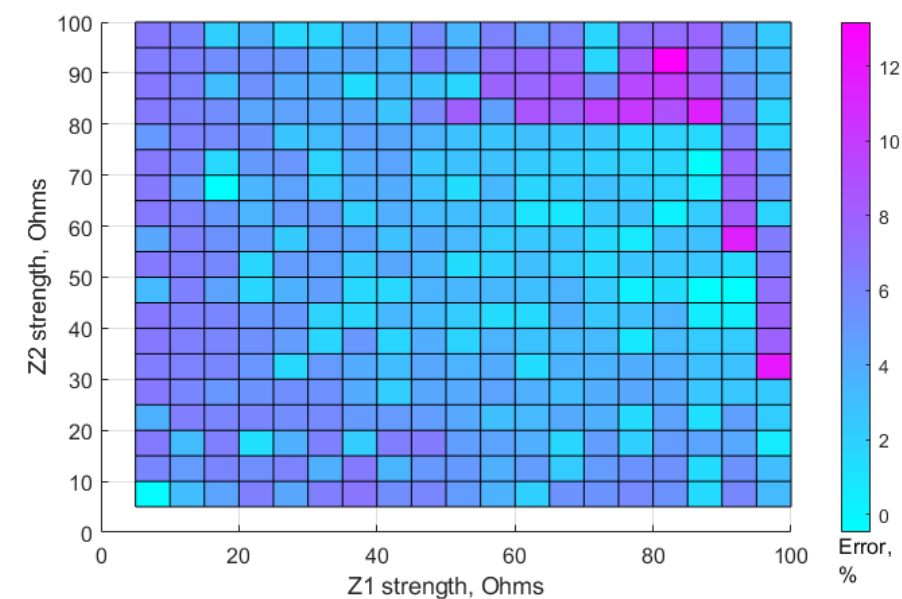
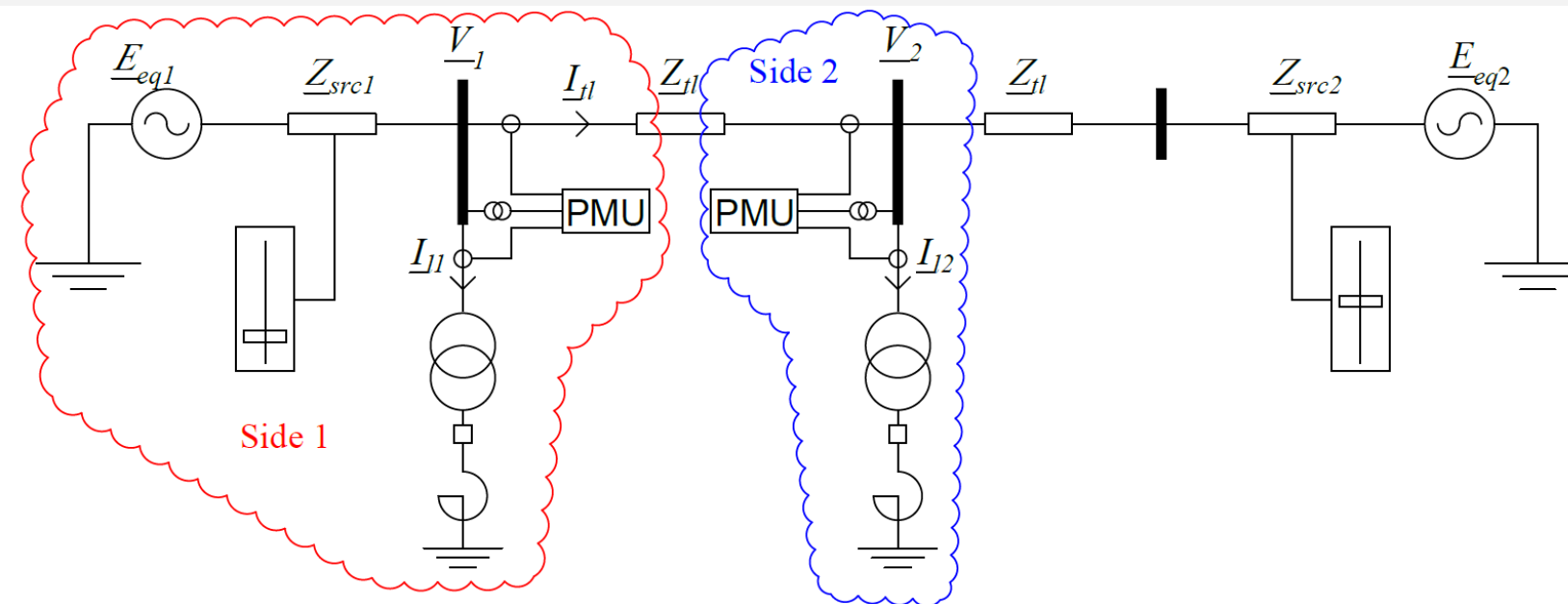
- IEEE 39 bus benchmark system is taken as a basis for larger system studies to investigate the out-of-step protection performance.
- Implementation of wind power plants into the network.
- Two scenarios are demonstrated in the network:
 - Case study A, in red, to represent swings between two system parts.
 - Case study B, in blue, to represent swings between single machine – infinite bus.



TESTING RESULTS

Impedance computation

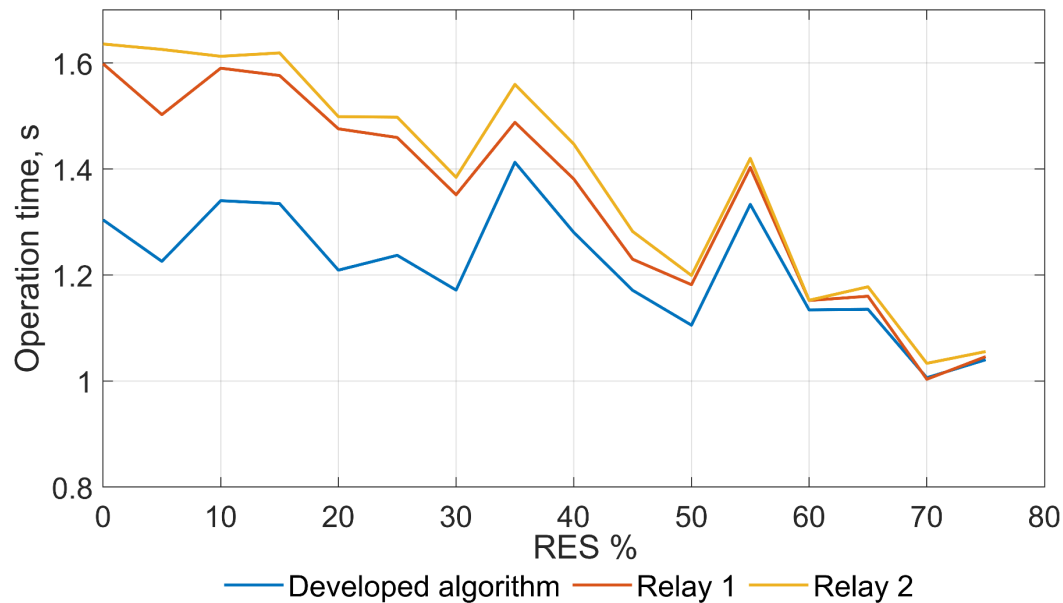
- To test and validate the impedance computation part of the new algorithm a simple system was developed and the following results were obtained.



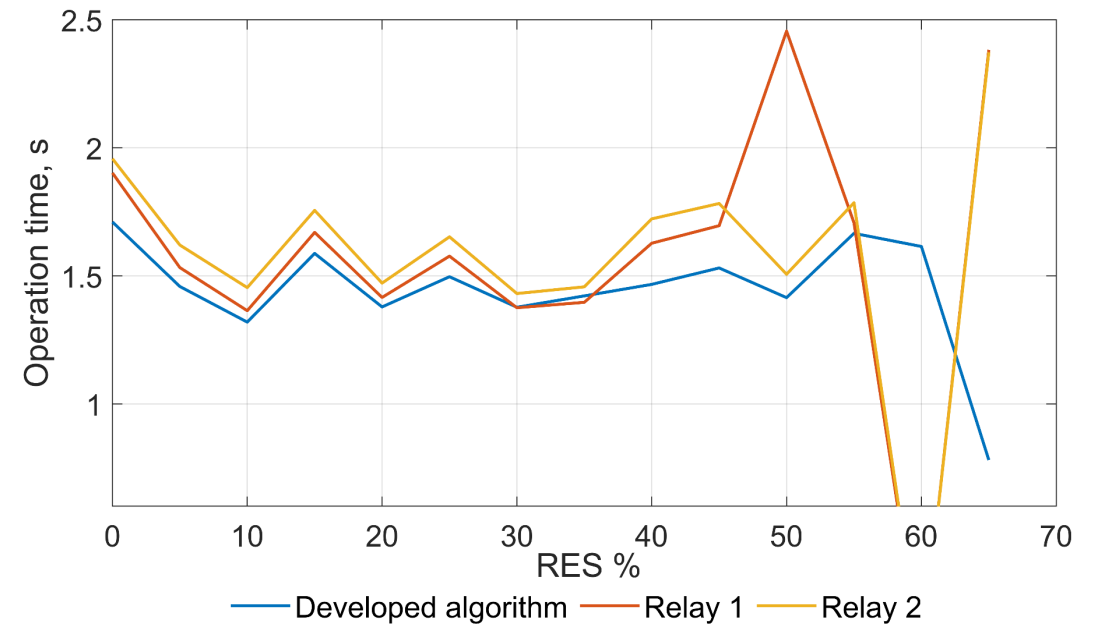
HIL TESTING OF THE ALGORITHM USING RTDS

Results for Case study A

Longer transmission line



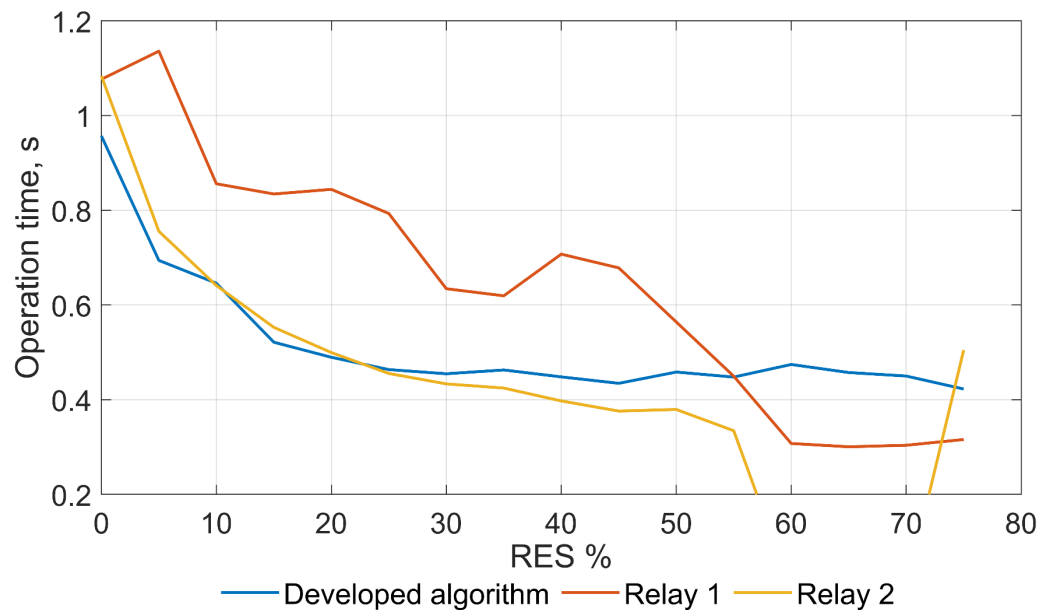
Shorter transmission line



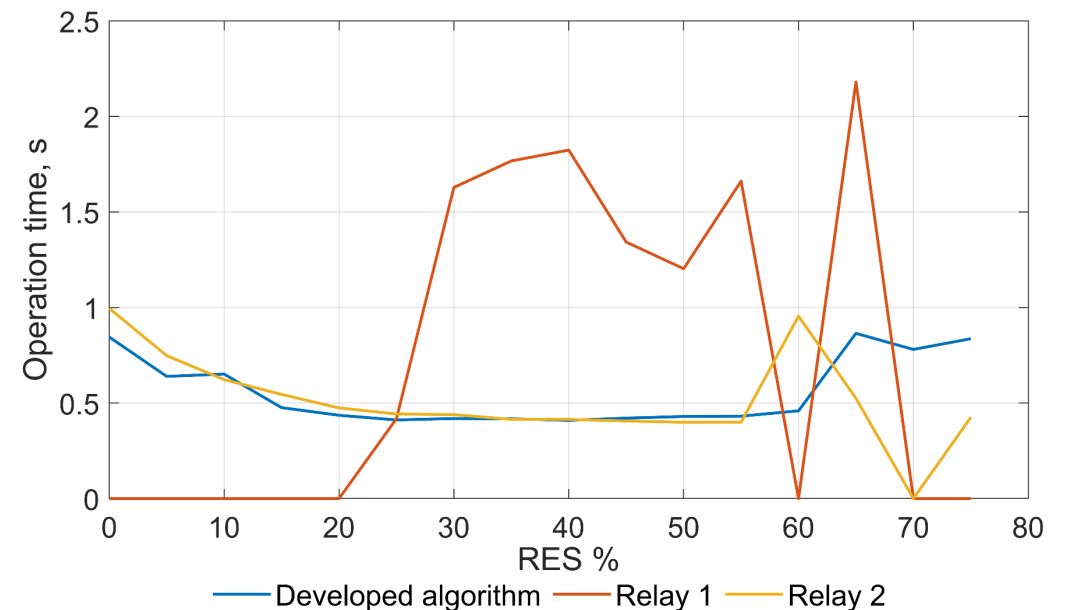
HIL TESTING OF THE ALGORITHM USING RTDS

Results for Case study B

Longer transmission line



Shorter transmission line



CONTRIBUTORS

